N FOR PATENT APPLICATION (WITH POWER OF DECLAR

ntinuation page, I hereby declare that: As an inventor named below or on any attac are as stated next to my name. My residence, post office address and citizera-

is attached hereto.



ORNEY)

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled TECHNIQUE FOR ELIMINATION OF PITTING ON SILICON SUBSTRATE DURING GATE STACK ETCH, the specification of which (check one):

was filed onas PCT in	ternational application no and was a	mended under PCT Article 19 on	·•	
I hereby state that I have reviewed and referred to above.	understand the contents of the above-identified sp	ecification, including the claims, as amend	led by any am	endment
I acknowledge the duty to disclose to the matter claimed in this application, as "mate	ne U.S. Patent and Trademark Office all informati eriality" is defined in Title 37, Code of Federal R	ion known to me to be material to the pater degulations § 1.56.	ntability of the	subject
certificate or § 365(a) of any PCT internat	under Title 35, United States Code, § 119 (a)-(d) ional application(s) designating at least one countributing below and on any attached continuation pating at least one country other than the United S	ry other than the United States of America page any foreign application for patent or i	listed below a inventor's cert	nd on any
Prior foreign/PCT application(s):	•		Priority C	laimed
(number)	(country)	(day/month/year filed)	Yes	No
(number)	(country)	(day/month/year filed)	Yes	No
application is not disclosed in any such pri	isted below and on any attached continuation page or application in the manner provided by the first	paragraph of Title 35, United States Code	e, § 112, I ack	nowledge
the duty to disclose to the U.S. Patent and Regulations § 1.56 which became available	or application in the manner provided by the first Trademark Office all information known to me to e between the filing date of such prior application	paragraph of Title 35, United States Code to be material to patentability as defined in	e, § 112, I ack Title 37, Cod	nowledge e of Federa
the duty to disclose to the U.S. Patent and Regulations § 1.56 which became available (application serial no.)	or application in the manner provided by the first Trademark Office all information known to me to	paragraph of Title 35, United States Code to be material to patentability as defined in	e, § 112, I ack Title 37, Coding date of this	nowledge e of Federa
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the duty to disclose to the U.S. Patent and Regulations § 1.56 which became available (application serial no.) [(application serial no.) [I hereby claim the benefit under Title 32 (provisional application no.) [(provisional application no.) [(provisional application no.) [(provisional application no.)	or application in the manner provided by the first Trademark Office all information known to me to e between the filing date of such prior application (filing date) (filing date) (filing date) (filing date) (filing date) (filing date)	paragraph of Title 35, United States Code to be material to patentability as defined in and the national or PCT international filin (status - pending, patented or a (status - pending, patented or a test provisional application(s) listed below:	e, § 112, I ack Title 37, Cod g date of this abandoned) abandoned)	nowledge e of Federa application

true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first joint inventor: Pai-Hung Pan

Inventor's signature

Residence: Boise, Idaho

Citizenship: U.S.A.

Post Office Address: 2773 East Migratory Drive, Boise, Idaho 83706

DECLARATION FOR PATENT APPLICATION

(continuation page)

AME DISPUSA

Invention title: TECHNIQUE FOR ELIMINA OF PITTING ON SILICON SUBSTRATE DURING COMESTACK ETCH

Inventor name(s) appearing on first declaration page: Pai-Hung Pan

Additional original, first and joint inventor(s):

Full name of second joint inventor: Louie Liu	Date 10-01-96
Residence: Meridian, Idaho	
Citizenship: Taiwan	
Post Office Address: 1412 N. Sandlin Avenue, Meridian, Idaho 83642	
Full name of third joint inventor: Ravi Iyer Inventor's signature	
Residence: Boise, Idaho	, ,

Citizenship: India Post Office Address: 5600 S. Fuchsia, Boise, Idaho 83705